



SERIAL
CABLES



Product Roadmap

H2 2026/H1 2027

Prepared by:

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EXECUTIVE SUMMARY

Serial Cables is giving PCIe 6.0 and 7.0 design teams a single vendor path from SI spec to validated bench, closing the gap between device availability and host platform readiness.

Our H2 2026 to Q2 2027 roadmap spans host cards, adapters, retimers, active and passive cables, and expansion chassis across CDFP, MCIO, and EDSFF, including a PCIe 7.0 host card & retimer roughly a year ahead of the vendor's PDK.

1.6T OSFP-XD and OSFP networking active and passive assemblies round out the portfolio to address next generation hyperscale architectures.

In house tooling and owned China manufacturing let us deliver any of these in **low MOQs**, as few as 50 units without lock in or six month lead times.

NEW HOST CARDS & ENCLOSURES

Type	Application	Product	Interface	When
Host Card	PCIe 6.0	Atlas3 CDFP host card (HHHL) 32 Lanes	x16 AIC to x16 CDFP	Oct 2026
Host Card	PCIe 6.0	Atlas3 2* x8 MCIO host card (HHHL) 32 Lanes	x16 AIC to 2* 8x MCIO	Nov 2026
Dev Board	PCIe 6.0	Javelin development board	2x x8 QSFP-DD, x16 CDFP, 4x x8 MCIO, x16 straddle	Nov 2026
Host Card	PCIe 6.0	Atlas3 MCIO host card 64 lanes	x16 AIC to 2x 2* 8x MCIO, Straddle Mount	Nov 2026
Enclosure	PCIe 6.0	"Tarantula" 8-slot expansion chassis	8x x16 AIC, power management tray	Dec / Q1 2027
Host Card	PCIe 6.0	Atlas3 OSFP-XD host card (FHHL)	x16 AIC to x16 OSFP-XD	Q1 2027
Enclosure	PCIe 6.0	CDFP expansion chassis	x16 CDFP to x16 AIC, PCIe / CXL	Q1 2027
Host Card	PCIe 7.0	Atlas7 host card (extended FHFL)	x16 AIC to x16 straddle + 4x x8 MCIO	Q2 2027

NEW ADAPTERS & RETIMERS

Type	Application	Product	Interface	When
Adapter	PCIe® 6.0	EDSFF passive adapter (AIC)	x16 AIC to 2x x8 EDSFF, NVMe / CXL	Sept/Oct 26
Adapter	PCIe® 6.0	CDFP active adapter (AIC)	x16 CDFP to x16 AIC, active cable support	Sept/Oct 26
Adapter	PCIe® 6.0 or OSFP-XD MSA	OSFP-XD active adapter (AIC)	x16 OSFP-XD to x16 AIC, active cable support	Sept/Oct 26
Adapter	PCIe® 6.0	CDFP x16 Slot Adapter	x16 CDFP to x16 slot, active cable support	Q1 2027
Adapter	PCIe® 6.0 or OSFP-XD MSA	OSFP-XD x16 Slot Adapter	x16 OSFP-XD to x16 slot, active cable support	Q1 2027
Retimer	PCIe® 6.0	CDFP retimer board (HHHL)	x16 AIC to x16 CDFP, PCIe / CXL	Q1 2027
Retimer	PCIe® 6.0 or OSFP-XD MSA	OSFP-XD retimer board (FHHL)	x16 AIC to x16 OSFP-XD, PCIe / CXL	Q1 2027
Retimer	PCIe® 7.0	Retimer Board	TBD	H1 2027

NEW CABLES

Type	Application	Product	When
AOC (Active Cable)	PCIe 6.0	CDFP x16 AOC, w/ sidebands	Q1 2027
ACC (Active Cable)	PCIe 7.0	CDFP x16 ACC, 5m w/ Semtech silicon	Q1 2027
ACC (Active Cable)	PCIe 6.0	CDFP x16 ACC, 5m w/ Phison silicon	Sept/Oct 26
DAC	OSFP-XD MSA	1.6T OSFP-XD DAC, up to 2m	Sept/Oct 26
DAC	PCIe 6.0	OSFP-XD DAC, up to 2m	Sept/Oct 26
ACC (Active Cable)	OSFP-XD MSA	1.6T OSFP x8 ACC, 2.5m / 3m	Sept/Oct 26
DAC	PCIe 7.0	MCIO x8 to 2x x8	Sept/Oct 26
DAC	PCIe 7.0	MCIO x8 to EDSFF 2x x4	Sept/Oct 26
DAC	PCIe 7.0	MCIO x8 to MCIO x8	Sept/Oct 26



THE SERIAL CABLES DIFFERENCE



WHAT SLOWS DOWN YOUR DESIGN?

Spotty PCIe 6.0/7.0 Host Availability

PCIe 6.0 drives and devices start shipping but PCIe 6.0 host platforms aren't available

Poor New FF & Protocols Support

New form factors (E3, E1, CXL 3.0) arrive before enclosures and fixtures exist to support them

Specs Evolving at Neck Breaking Speed

Engineering teams need to start designing for PCIe 7.0 and its tough SI needs as PCIe 6.0 rolls out.

Unexpected Hardware gaps

Every gap in your next gen system testing slows you down, ballooning your costs & delaying your go-to-market strategy.

High MOQs, Inflexible or slow vendors

Large suppliers are slow & require volume: they cannot make minor tweaks at the speed of EVT / DVT: their focus is on MP



SERIAL CABLES ACCELERATES YOUR LAB

No PCIe 6.0/7.0 Host Availability

PCIe 6.0 drives and devices start shipping but PCIe 6.0 host platforms aren't available



PCIe 6.0/7.0 Host Cards

Start testing next gen drives and appliances ahead of host availability with our host card products

PCIe 7.0 Host Adaptors available in Q2 2027

Poor FF & Protocols Support

New form factors (E3, E1, CXL) arrive before enclosures and fixtures exist to support them



FF Adapters & Cables

Toolchain agnostic JBOF, storage adaptors & cables to setup your test bench and stay on track

Specs Evolving at Neck Breaking Speed

Engineering teams need to start designing for PCIe 7.0 and its tough SI needs as PCIe 6.0 rolls out.



PCIe 7.0 / 6.0 Ready

Validated PCIe 7.0 cables and connectivity coverage to fit your testing needs

Variety of silicon options from early silicon developers with continuous support

Unexpected Hardware gaps

Every gap in you're your next gen system testing slows you down, balloons your costs & drags you to market



Custom Design

Custom design and manufacture capabilities to swiftly address tough layout or SI challenges and keep you on track.

Inflexible/slow vendors

Large suppliers are slow & require volume: they cannot make minor tweaks at the speed of EVT / DVT: their focus is on MP



Fast, Small Runs

Ultra low MOQ, Quick turnaround (weeks) for stock / custom cables, adapters, and host interfaces for PCIe 6.0 & 7.0 applications



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SERIAL CABLES UNIQUE VALUE

Product Line	Unique value?
 Cables	• 8 weeks from concept to cable (vs. 6 months) • Pinout changes in weeks rather than months • MOQ as low as 50 units (vs. 5,000) • Highest-quality build • In-house tooling • Own China manufacturing facilities
 Boards & adapters	• Standard, hard-to-find, and custom designs • No minimum order quantity • Latest PCIe speeds a year ahead of the PDK, at a fraction of the cost, accelerating product development
 Enclosures	• PCIe 6.0 JBOF with built-in Quarch power analysis • Best of both worlds: utility and T&M
 Testing, Quarch	• Industry-standard power monitoring and test • Correlates power with protocol • AI/ML and SSD applications • Scope and analyzer probes
 Custom Design	• Bespoke cables, boards, and fixtures designed to your topology and signal-integrity targets • Rapid prototyping in weeks: iterate on pinouts, bending, lengths, and connectors without vendor lock-in • Low-volume runs (tens of units) for bring-up, characterization, and qualification • One vendor for design, tooling, and manufacturing = fewer hand-offs, faster lab turnaround

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Q&A

THANK YOU

